

Docket No.: 060188-076

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kenji YONEDA

Application No.: 10/774,522

Filed: February 10, 2004

Customer Number: 20277

Confirmation Number: 9901

Group Art Unit: 2891

Examiner: SARKAR, Asok K.

For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE

Mail Stop Amendment
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☒ No additional fee is required.
☐ Applicant is entitled to small entity status under 37 CFR 1.27
☐ Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	23	23	0	\$50.00 =	\$0.00
Independent Claims	5	5	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

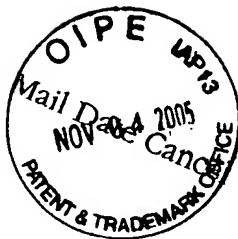
for *[Signature]* #46,692
Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:dmd
Facsimile: 202.756.8087
Date: November 7, 2005

Please recognize our Customer No. 20277 as our
correspondence address.

Docket No.: 060188-0768

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

The Application of

Kenji YONEDA

Application No.: 10/774,522

Filed: February 10, 2004

: Customer Number: 20277

: Confirmation Number: 9901

: Group Art Unit: 2891

: Examiner: SARKAR, Asok K.

For: METHOD FOR FABRICATING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR
DEVICE

AMENDMENT

Mail Stop Amendment
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 5, 2005, having a three-month shortened
statutory period for response set to expire on November 5, 2005, please amend the above-
identified application as follows: